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| Name:  Click or tap here to enter text. | Date:  Click or tap to enter a date. |

# Summing Amplifier

The fact that the inverting input of the op-amp, when arranged in an inverting amplifier configuration, acts as a virtual ground allows one to inject several currents into that point without having any interaction between the signals. The signals sum cleanly and we can create an amplifier where the output voltage is related to a (perhaps weighted) sum of the input voltages.

![Diagram, schematic

Description automatically generated]()

Consider the circuit shown above. Let’s step through a quick analysis of this circuit to determine the output voltage as a function of the input voltages , .

## Potential at inverting input.

Keeping in mind the op-amp golden rules, what is the potential at the inverting input (-) of the op-amp?

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| Click or tap here to enter text. |

## Input currents.

Using Ohm’s law, write expressions for the currents thru resistor , thru resistor , and thru resistor . Your answers will depend upon the input voltages … .

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| Click or tap here to enter text. |

## Feedback current.

Using Ohm’s law, write an expression for the current thru the feedback resistor . Your answer will depend upon the output voltage . Be careful here to get the sign correct.

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## Op-amp input current.

Keeping in mind the op-amp golden rules, how much current is drawn at the inverting input of the op-amp?

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| Click or tap here to enter text. |

## Current relationship.

Using Kirchoff’s node rule and the previous answer, write an expression for the current thru the feedback resistor in terms of the input currents , , … .

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## Voltage relationship.

Combining the previous results, write an expression for the output voltage that only depends on the input voltages … and resistors and .

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# Averaging Amplifier

In the previous circuit, suppose that the feedback resistor is and that there are inputs. What values (in terms of ) should be chosen for the input resistors , and so that the output voltage is equal to the (negative of the) **average value** of the inputs and ?

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# Digital to Analog Converter

![Diagram, schematic

Description automatically generated]()A simple 4-bit digital to analog converter is shown in the adjacent circuit. This system can represent , or , different voltage levels depending on the state of the four switches. Take the common input voltage to be volts. Each input can be considered to be a simple high/low logic level that represents a 1 or 0 for that particular bit. Note that the input resistors vary by factors of 2. The gain for the lowest path is , or unity. This input is used for the most significant bit of the input word (MSB). The next input shows a gain of , or . The third input shows a gain of , and the final (top) input shows a gain of . The final input has the lowest gain and is used for the least significant bit of the input word (LSB). If the input word had a higher resolution (i.e., more bits), extra channels would be added, each having half the gain of the preceding input. To better understand the conversion process, let's consider a few representative inputs and outputs.

## Output for

The circuit above shows all of the switches connected to the source. Suppose instead that all of them are connected to ground. What will be the value of the output in this case?

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## Output for

Suppose that the topmost switch is connected to the source and the remaining three are connected to ground. What will be the value of the output in this case?

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## Output for 1111.

Suppose that all of the switches are connected to the source in the same manner as shown in the above figure. What will be the value of the output in this case?

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| Click or tap here to enter text. |